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Applicant: Sheu, *et al.*

Docket No.: TSM03-0140

Serial No: 10/619,828

Art Unit: 2829

Filed: July 15, 2003

Date Mailed: June 23, 2005

TITLE: Self-Aligned MOSFET having an Oxide Region below the Channel

Certificate of Mailing via First Class Mail (1 page)

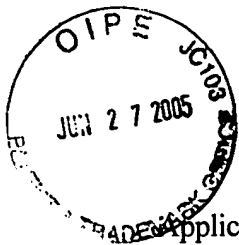
Information Disclosure Statement (1 original & 1 copy = 2 pages)

IDS Form PTO/SB/08b (1 page) citing 2 references

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Sheu, *et al.* Docket No.: TSM03-0140
Serial No.: 10/619,828 Art Unit: 2829
Filed: July 15, 2003 Examiner: Dana Farahani
For: Self-Aligned MOSFET having an Oxide Region below the Channel

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants wish to bring to the attention of the U.S. Patent and Trademark Office the information noted on the enclosed PTO Form PTO/SB/08b, which may be considered material to the examination of the above-identified application.

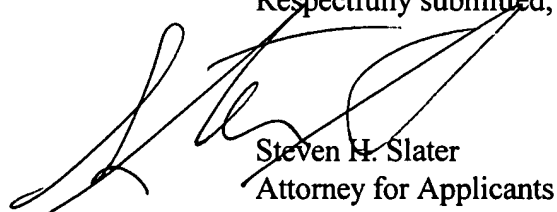
This Information Disclosure Statement is submitted under 37 C.F.R. §1.97(c) together with a \$180.00 fee under 37 C.F.R. §1.17(p) after the C.F.R. §1.97(b) time period, but before final action or notice of allowance, whichever occurs first.

Please charge the required fee of \$180.00 and any additional amount, or credit any overpayment to Deposit Acct. No 50-1065 of the below mentioned firm.

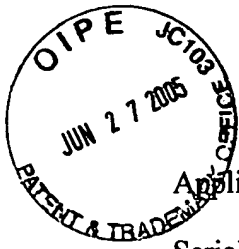
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June 23, 2005

Respectfully submitted,


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Date of Deposit: June 23, 2005

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Respectfully submitted,


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